

**IN THE SPECIFICATION:**

Replace the paragraph at page 92, line 23 to page 93, line 7 with the following amended paragraph:

The driver circuit is composed of logic circuits such as a buffer circuit, the shift ~~resister~~ register circuits or the like, as well as a sampling circuit formed of an analog switch, or the like. In Fig. 21B, the TFTs for forming these circuits are illustrated to have a single-gate structure in which only one gate electrode is provided between a pair of source and drain regions. However, a multigate structure in which a plurality of gate electrodes are provided between a pair of source and drain regions may also be used.